



Mr. ShyamBahadurVerma

Assistant Professor

Mr. ShyamBahadurVerma is currently working as Assistant Professor in the department of Computer Science and Engineering, BIT Meerut. He has more than 12 years of teaching and 1 year industry experience. His current research interests includes Image Processing, Wireless Sensor Networks and Network Security.

He has completed his Master of Engineering (CSE) from NITTTR Chandigarh in 2017 and Bachelor of Engineering (CSE) from Dr. K.N.M.I.E.T Modinagar in 2003.

He has taught various subject like Automata Theory, Compiler Design, Algorithms and Data Structures, Cryptography and Network Security, Image Processing and Parallel Algorithms.

He has published many research papers in National and International Journals and guided more than 30 undergraduate projects.

He has attended various faculty development programs like Wipro Mission 10X (Dale Carnegie Training), workshop on Cloud Computing, scilab and many more.